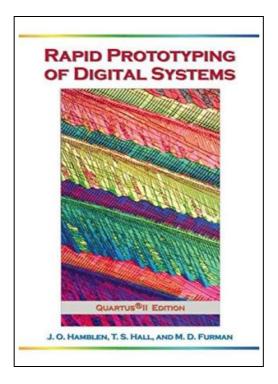
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This is actually the best book i actually have go through right up until now. It generally will not price an excessive amount of. I discovered this book from my dad and i suggested this book to understand.

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Springer-Verlag New York Inc., United States, 2005. Mixed media product. Condition: New. Quartus(r) II edition. Language: English . Brand New Book ***** Print on Demand ******. Rapid Prototyping of Digital Systems: Quartus II Edition provides an exciting and challenging laboratory component for undergraduate digital logic and computer design courses using FPGAs and CAD tools for simulation and hardware implementation. The more advanced topics and exercises also make this text useful for upper level courses in digital logic, programmable logic, and embedded systems. This new version of the widely used Rapid Prototyping of Digital Systems, Second Edition, now uses Altera s new Quartus II CAD tool and includes laboratory projects for Altera s UP 2 and the new UP 3 FPGA board. Rapid Prototyping of Digital Systems: Quartus II Edition includes four tutorials on the Altera Quartus II and NIOS II tool environment, an overview of programmable logic, and IP cores with several easy-to-use input and output functions. These features were developed to help students get started quickly. Early design examples use schematic capture and IP cores developed for the Altera UP FPGA boards. VHDL is used for more complex designs after a short introduction to VHDL-based synthesis. New to this edition is an overview of System-on-a-Programmable Chip (SOPC) technology and SOPC design examples for the UP3 using Altera s new NIOS II Processor hardware and C software development tools.



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