Read PDF

HIGH FREQUENCY INTERCONNECT CHARACTERIZATION AND MODELING



Condition: New. Publisher/Verlag: VDM Verlag Dr. Müller | For VLSI On-Chip Interconnects and RF Package Wire Bonds | Continuous scaling of transistors combined withincreased chip area results in the ratio of globalwire delay to gate delay increasing at a super-linearrate. Simple RC models have become inadequate forsimulation of VLSI circuits. In addition, parasiticinductance and capacitance of IC packages impose limits on the circuit performance at RF frequencies. This book presents modeling of on-chip inductance forchips with ground grids that emulate those used...

Read PDF High Frequency Interconnect Characterization and Modeling

- Authored by Qi, Xiaoning
- Released at -



Filesize: 5.28 MB

Reviews

I just began reading this pdf. It is actually writter in straightforward words instead of hard to understand. Once you begin to read the book, it is extremely difficult to leave it before concluding.

-- Jensen Bins

Very beneficial for all class of folks. Indeed, it can be perform, nevertheless an interesting and amazing literature. I discovered this ebook from my i and dad suggested this pdf to find out.

-- Leatha Luettgen Sr.

Related Books

- Silly Jack and the Beanstalk: Green A/1b
- Zip and Zap and the Thing: Yellow A/1c
- Words and Rhymes for Kids: A Fun Teaching Tool for High Frequency Words and Word Families
- Oxford Reading Tree Read with Biff, Chip, and Kipper: Phonics: Level 2: The Fizz-buzz (Hardback)
- Oxford Reading Tree Read with Biff, Chip, and Kipper: Phonics: Level 6: Ice City (Hardback)