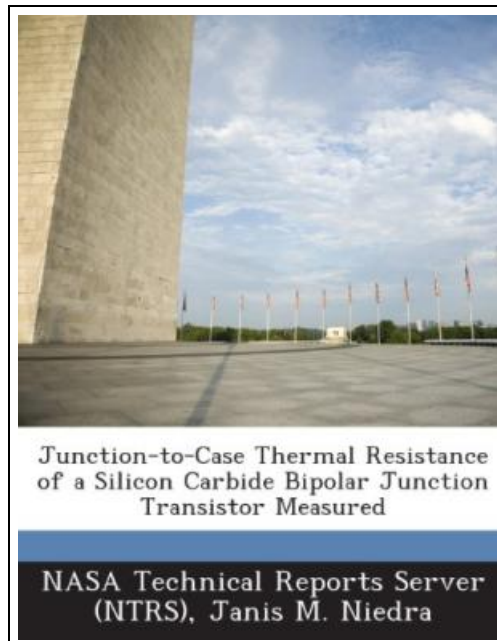


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Bibliogov, United States, 2013. Paperback. Condition: New. Language: English . Brand New Book ***** Print on Demand *****. Junction temperature of a prototype SiC-based bipolar junction transistor (BJT) was estimated by using the base-emitter voltage (V_{BE}) characteristic for thermometry. The V_{BE} was measured as a function of the base current (I_B) at selected temperatures (T), all at a fixed collector current (I_C) and under very low duty cycle pulse conditions. Under such conditions, the average temperature of the chip was taken to be the same as that of the temperature-controlled case. At increased duty cycle such as to substantially heat the chip, but same I_C pulse height, the chip temperature was identified by matching the V_{BE} to the thermometry curves. From the measured average power, the chip-to-case thermal resistance could be estimated, giving a reasonable value. A tentative explanation for an observed bunching with increasing temperature of the calibration curves may relate to an increasing dopant atom ionization. A first-cut analysis, however, does not support this.



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