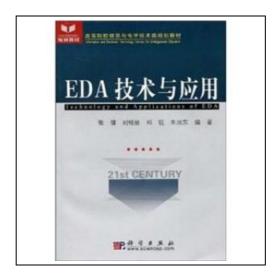
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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Pages Number: 285 Publisher: Science Pub. Date :2008-08-01 version 1. This book systematically describes the EDA technology are mainly related to large-scale programmable logic devices. hardware description languages. development tools. software and experimental development four aspects of the system. Altera programmable logic devices introduces the company's typical device. the hardware description language introduced has become the international standard VHDL. software development tools. introduced the industry's most popular Quartus II 7.2. all the design examples are done in hardware on the DE2 board verify. The book is divided into 10 chapters. followed by introduction of EDA project development process and top-down design approach. large-scale programmable logic device structures. principles and typical device. Quartus II 7.2 operating basis. VHDL language based. VHDL The main description of the statement. VHDL design to share. basic logic circuits VHDL reference design. VHDL description of the finite state machine design. digital electronic clock. simple asynchronous communication module. serial memory access controller and serial-controlled LCD display controller. etc. Design and simulation tool Modelsim is used. This book can serve as electronics, automation, computer classes, mechanical and electrical professional undergraduate and graduate students for the teaching of EDA and embedded systems or reference materials. but also as engaged in the design of digital systems. VLSI design and embedded System hardware design engineers and technicians of reference. Contents: Chapter 1 Overview 1.1EDA technology development process 1.2EDA technology research areas 1.3EDA software system consists of digital system design 1.5EDA 1.4 Technology Trends Summary Exercises Chapter 2 thinking and can 2.1 Overview of programming logic device development process 2.1.2PLD 2.1.1PLD 2.2 Classification of complex programmable logic device (CPLD) 2.3 field-programmable gate array (FPGA) 2.3.1FPGA the basic working principle 2.3.2FLEXI0K series...

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