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Condition: New. Publisher/Verlag: Scholar&apos;s Press | Methodology, Modeling, Implementation | This book proposes a methodology for implementing digital circuits and systems using asynchronous scheme called as self-timing. A methodology which results in average case performance vs. worst-case performance of the digital system without the global clocking overhead. The primary objective of the book is to give the readers an overview of the self-timing methodology, how to build smaller to medium scale digital circuits, modeling for HDL simulation and fast prototyping...

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- Authored by C. P., Jithesh
- Released at -



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