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DCT: VLSI IMPLEMENTATION USING SYSTOLIC ARRAY



Condition: New. Publisher/Verlag: LAP Lambert Academic Publishing | The discrete cosine transform (DCT) has been widely used in areas of speech and audio/video data compression. There are two traditional approaches to implementation of the DCT, implementations using butterfly structures or systolic arrays. Systolic array uses parallel pseudo-circular correlation structures as basic computational forms. The proposed algorithm can be mapped onto two linear systolic arrays with similar length and form that have a small number of I/O bandwidth that can be efficiently...

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- Authored by Agrawal, Ekta
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