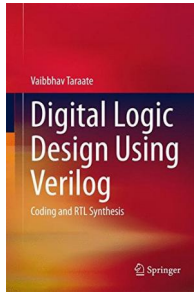


## Digital Logic Design Using Verilog: Coding and RTL Synthesis



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**(Brendan Doyle)**

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